



2171

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of	)	Attny Docket No.
	)	NVIDP316B/P001311
Thomas C. Poff et al.	)	
	)	Examiner: Le
Application No. 09/866,167	)	
	)	Art Unit: 2171
Filed: June 20, 2001	)	
	)	Date: December 23, 2004
For: HARDWARE ACCELERATOR FOR AN	)	
OBJECT-ORIENTED PROGRAMMING LANGUAGE)	)	

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on December 23, 2004.

Signed:

Erica L. Farlow

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §§ 1.56 AND 1.97(C)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

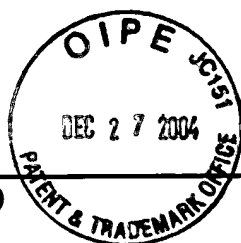
This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is being filed after the mailing date of a first Office Action. Accordingly, the Commissioner is hereby authorized to charge any fees due with the filing of this paper or credit any overpayment to Deposit Account 50-1351 (Order No. NVIDP316B).

Respectfully submitted,  
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<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Atty. Docket No.	Application No.:
	NVIDP316B	09/886,167
	Applicant:	
	Thomas C. Poff et al.	
Filing Date:	Group Art Unit:	
06/20/2001	2171	

**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	5,870,622	2/9/1999	Gulick et al.	395	800.35	10/10/1997
	B	5,923,892	7/13/1999	Levy	395	800.31	10/27/1997
	C	5,946,487	8/31/1999	Dangelo	395	705	6/10/1996
	D	5,664,162	9/2/1997	Dye	345	521	11/3/1994
	E	4,807,111	2/21/1989	Cohen et al.	364	200	6/19/1987
	F	5,918,051	6/29/1999	Savitzky et la.	395	683	7/19/1995
	G	5,544,357	8/6/1996	Huei	395	600	5/26/1995
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	K	5,877,764	3/2/1999	Feitelson et al.	345	347	10/23/1992
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	T	5,734,852	3/31/1998	Zias et al.	395	334	3/8/1996

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	U	WO/98/21655	5/22/1998	WIPO	G06F	11/00	X	

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	V	Case, "Implementing the Java Virtual Machine" Microprocessor Report, March 1996
	W	Wayner, "Sun Gambles on Java Chips" Byte, November 1996
	X	
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.